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(54) **CONTROL CIRCUIT OF PROCESSING REQUEST INFORMATION**

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(57) Abstract

PURPOSE: To obtain a control circuit which processes plural kinds of processing request information from a circuit interface part and a circuit data processing part in a queue buffer format and also processes processing requests in time series according to priority.

CONSTITUTION: Significant information received by a digital subscriber circuit interface part 3 is sent to the circuit data processing part 2 through a data bus 6 and stored in a data queue 9 in it or stored in the queue 9 after being processed upon occasion. The circuit data processing part 2 informs processing request information queue 4 of a processing request through a processing request data line 8. An interruption line 7 operates for the queue 4 successively from the processing request with top priority to send an interruption request to a main processing part 1. When requests have the same priority, the interruption line 7 operates in time series successively in arrival order to send the interruption request to the main processing part 11.

